

FIG.1

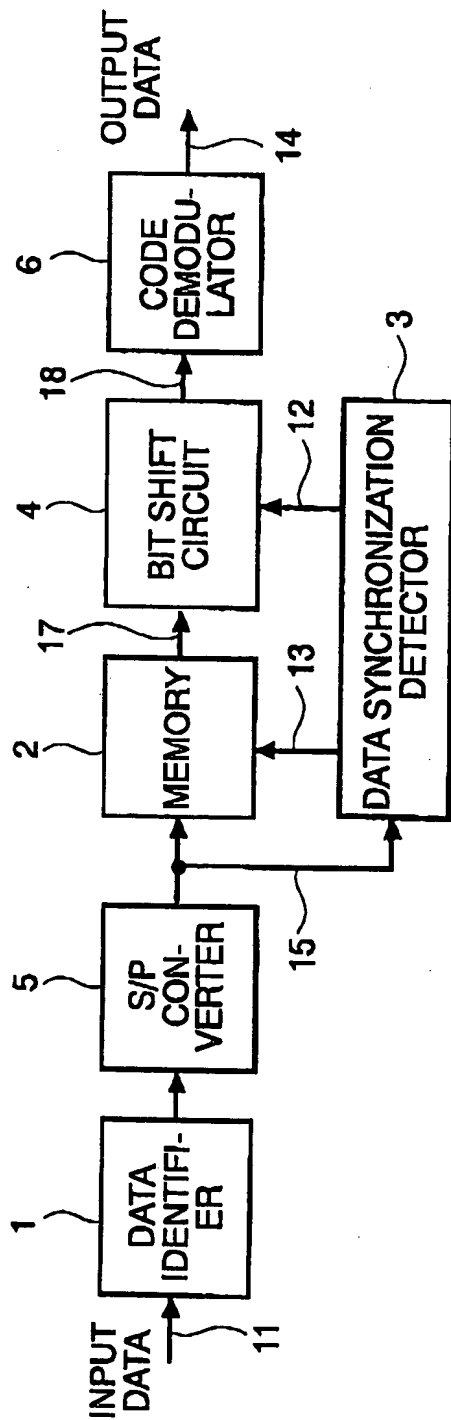


FIG.2

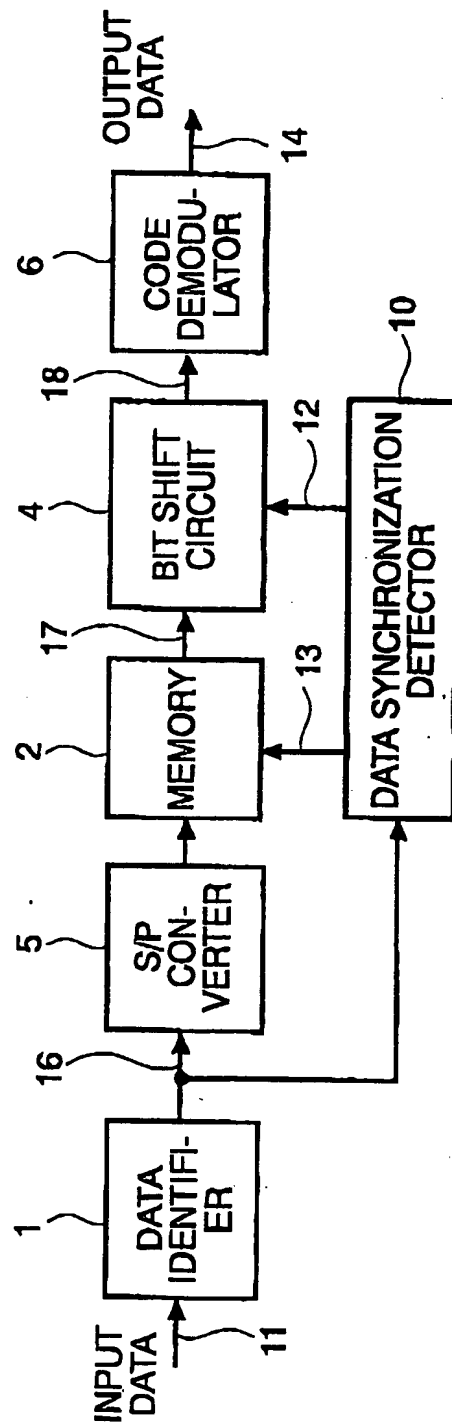


FIG.3

No.	CODE-MODULATION BLOCK (9 BITS)																		WHETHER OR NOT PATTERN IS PRESENT
	1	2	3	4	5	6	7	8	9	1	2	3	4	5	6	7	8	9	
1	0	0	0																NO
2		0	0	0															YES
3			0	0	0														YES
4				0	0	0													YES
5					0	0	0												YES
6						0	0	0											YES
7							0	0	0										NO
8								0	0	0									YES
9									0	0	0								YES
10	0	*	0	*	0														NO
11		0	*	0	*	0													NO
12			0	*	0	*	0												YES
13				0	*	0	*	0											NO
14					0	*	0	*	0										NO
15						0	*	0	*	0									YES
16							0	*	0	*	0								YES
17								0	*	0	*	0							YES
18									0	*	0	*	0						YES
19	1	0	0	0	0	0	1												YES
20		1	0	0	0	0	1												YES
21			1	0	0	0	0	1											YES
22				1	0	0	0	0	1										YES
23					1	0	0	0	0	1									NO
24						1	0	0	0	0	1								NO
25							1	0	0	0	0	1							YES
26								1	0	0	0	0	1						NO
27									1	0	0	0	0	1					NO
28	1	*	0	*	0	*	0	*	0	*	0	*	1						NO
29		1	*	0	*	0	*	0	*	0	*	0	*	1					NO
30			1	*	0	*	0	*	0	*	0	*	0	*	1				NO
31				1	*	0	*	0	*	0	*	0	*	0	*	1			YES
32					1	*	0	*	0	*	0	*	0	*	0	*	1		YES
33						1	*	0	*	0	*	0	*	0	*	0	*	1	NO
34							1	*	0	*	0	*	0	*	0	*	0	*	NO
35								1	*	0	*	0	*	0	*	0	*	0	NO
36									1	*	0	*	0	*	0	*	0	*	NO

FIG.4

GUARANTEED EQUAL TO
OR ABOVE THRESHOLD VALUE

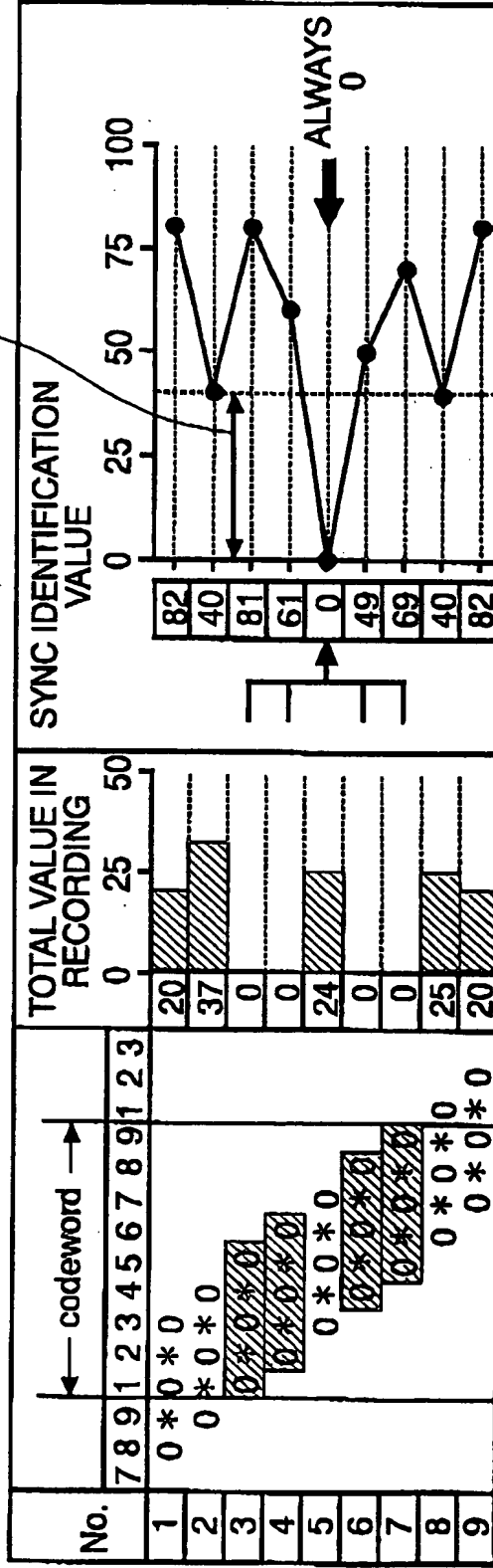


FIG.5

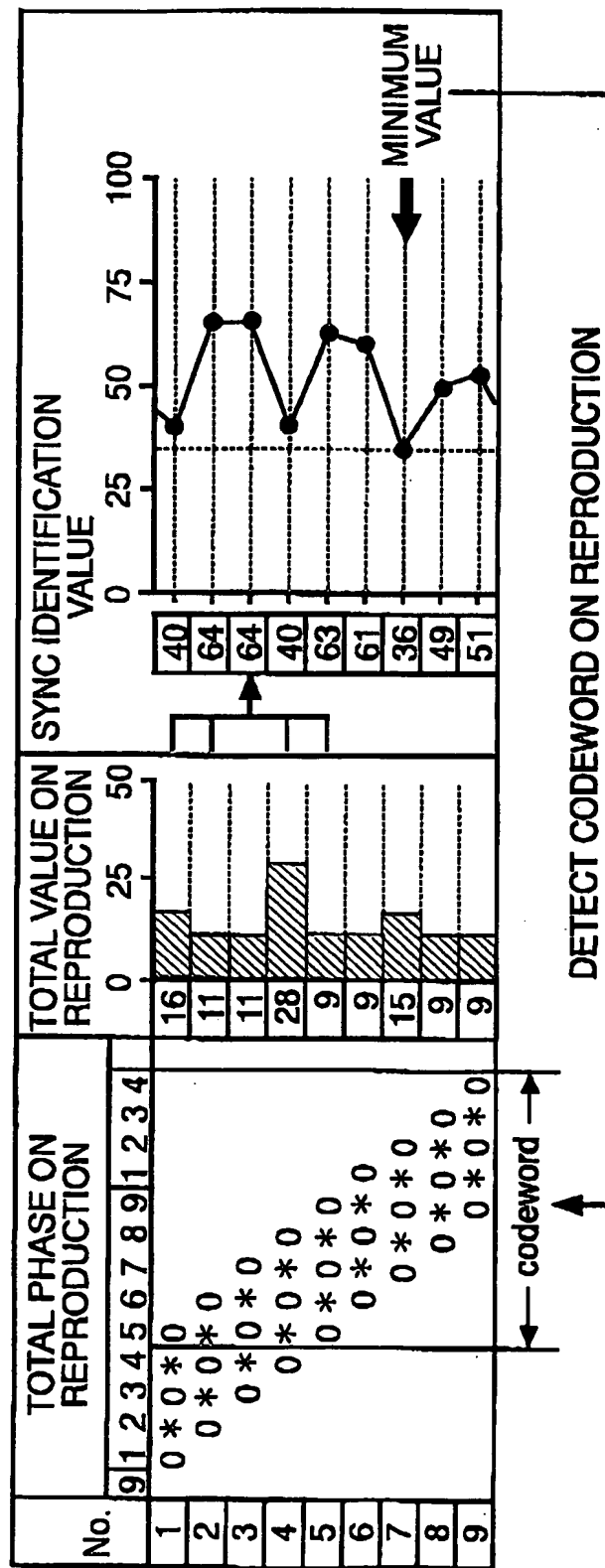


FIG.6

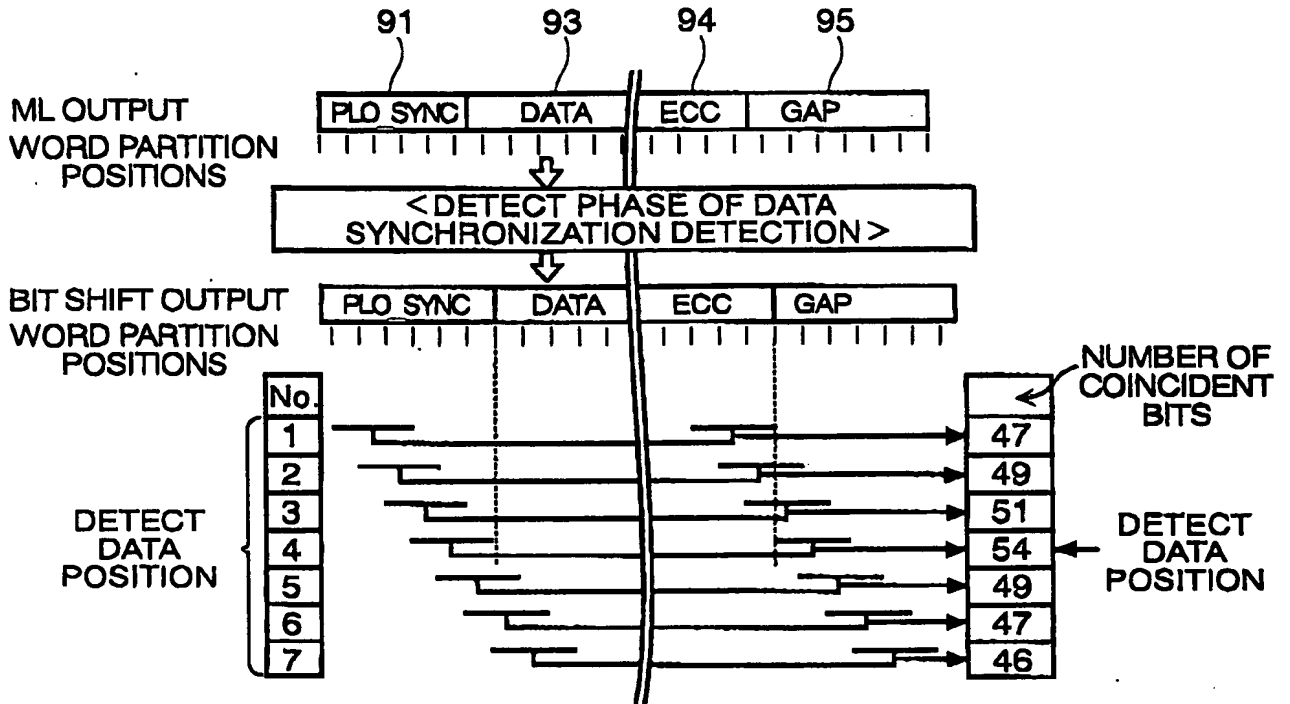


FIG.7

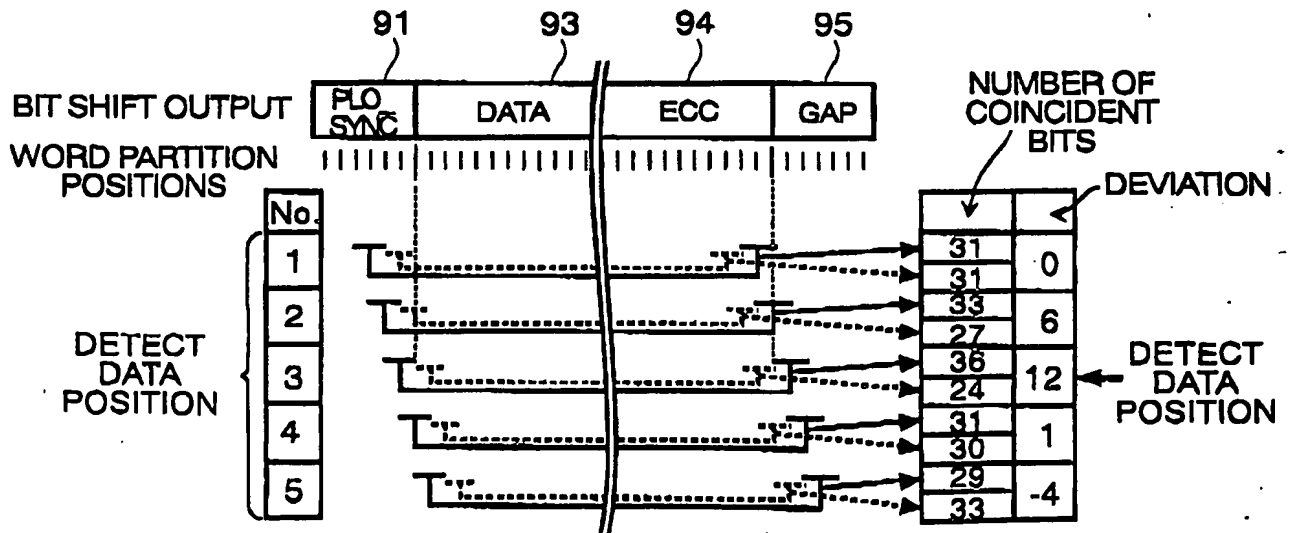


FIG.8

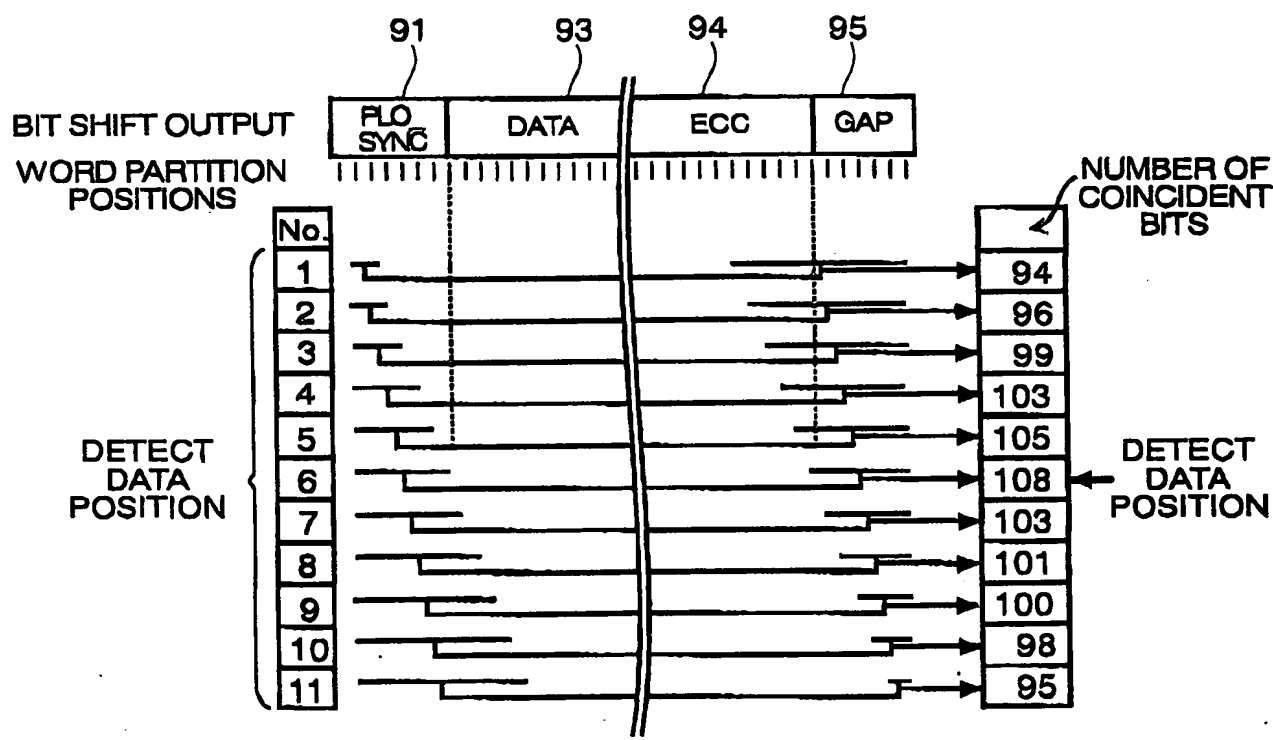


FIG. 9

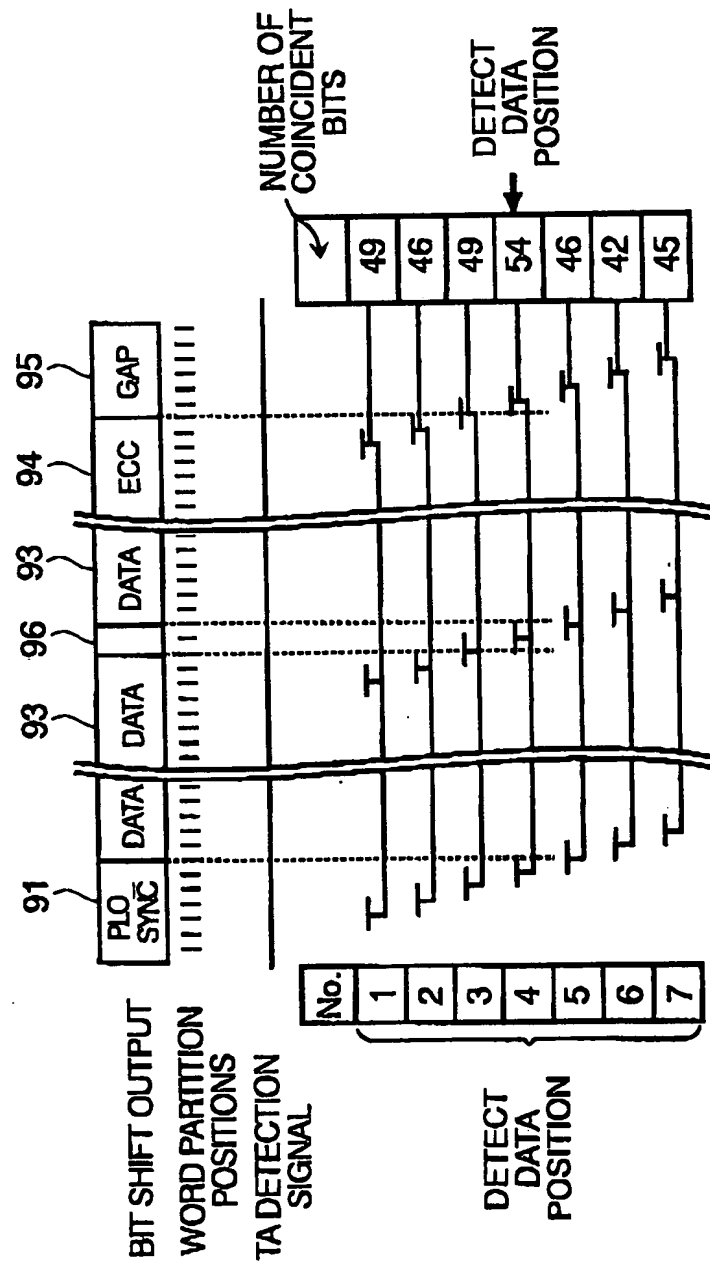


FIG.10

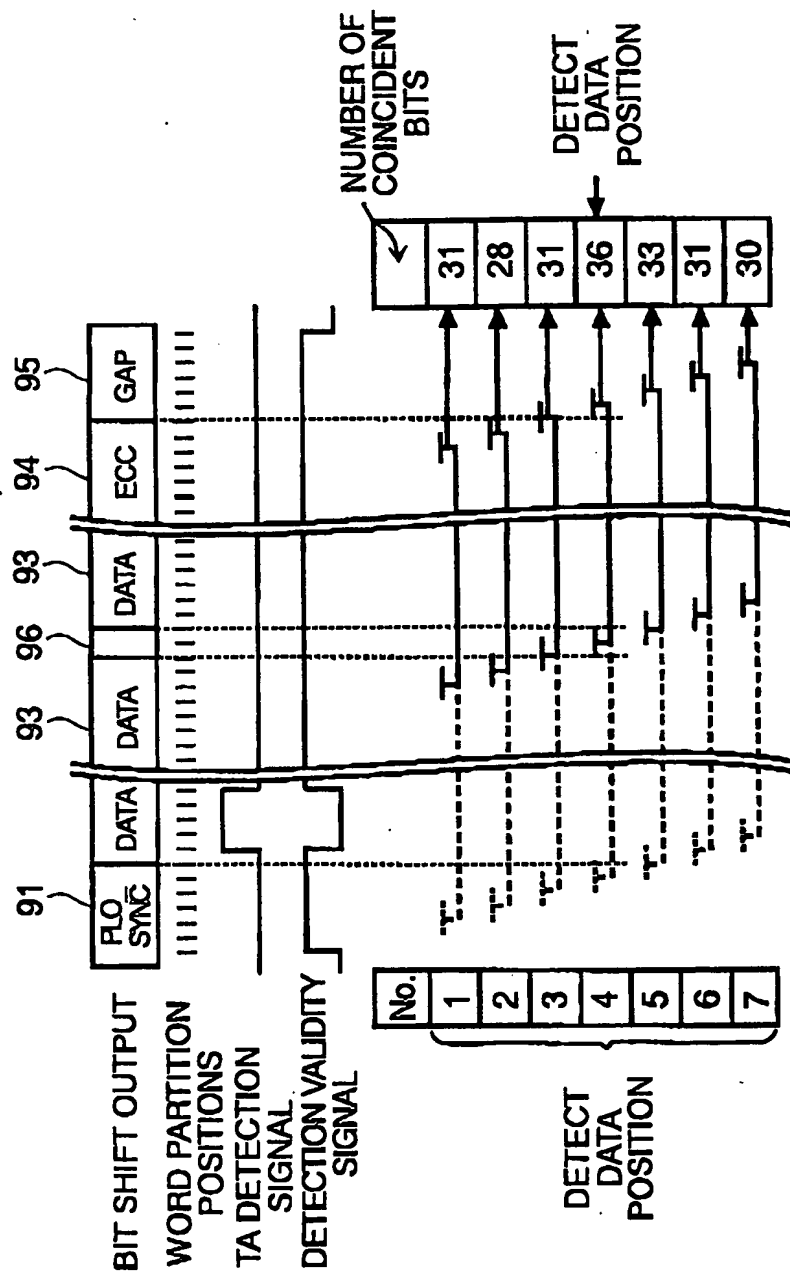


FIG.11

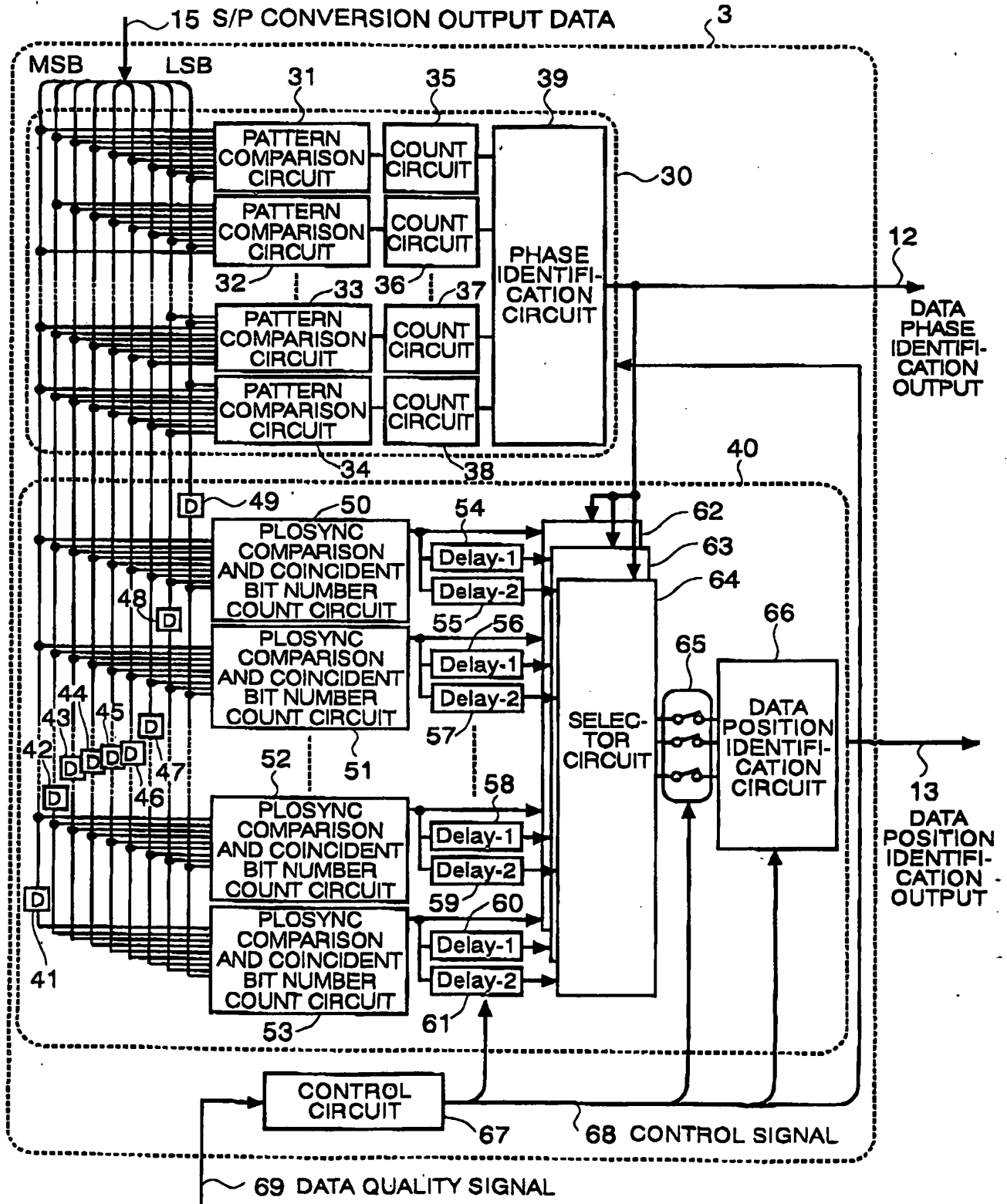


FIG.12

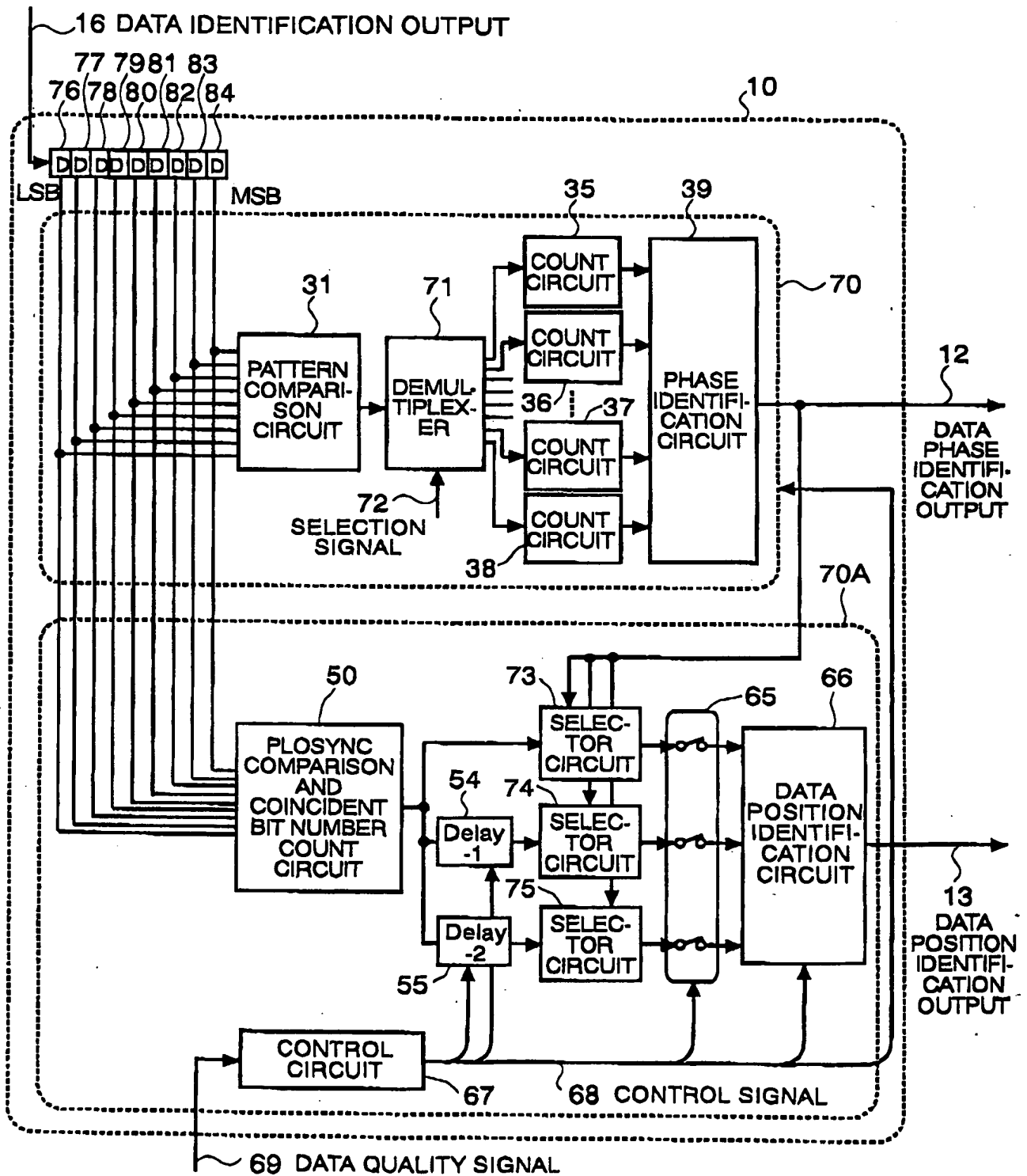


FIG.13

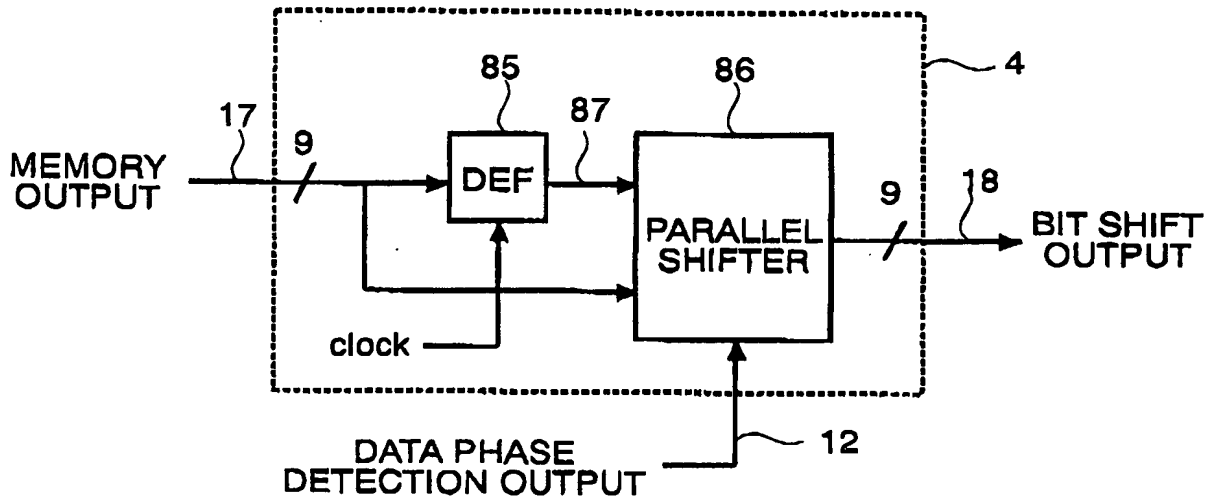


FIG.14

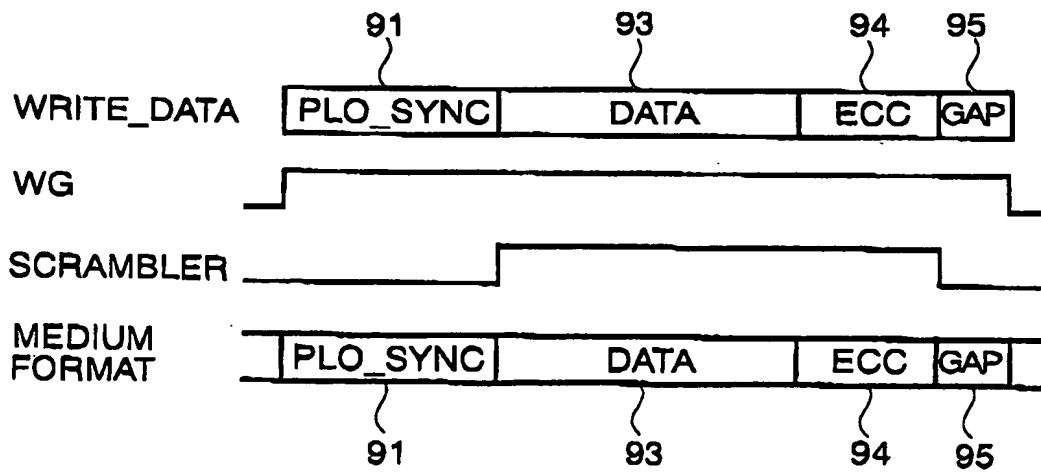
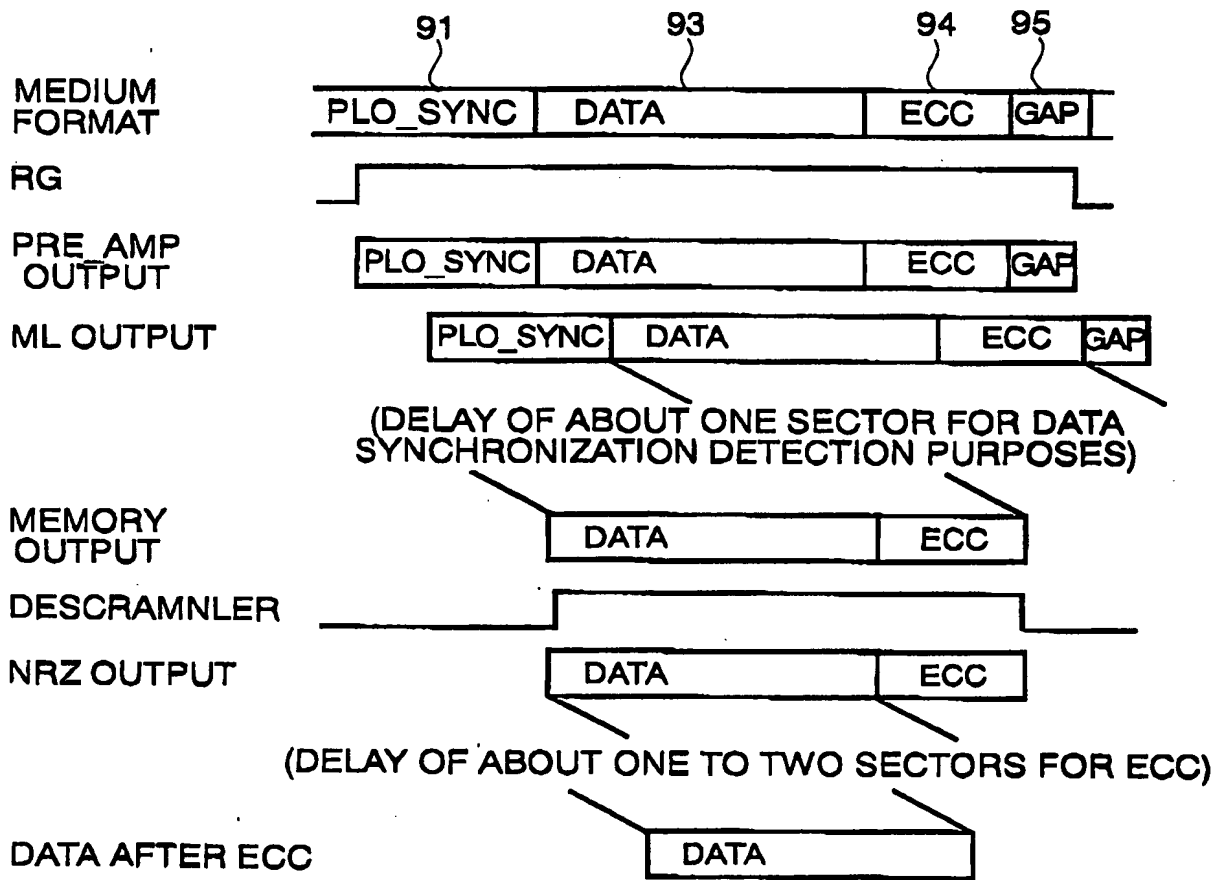


FIG.15



00930215-001501
T09T60"5T20E550

FIG.16

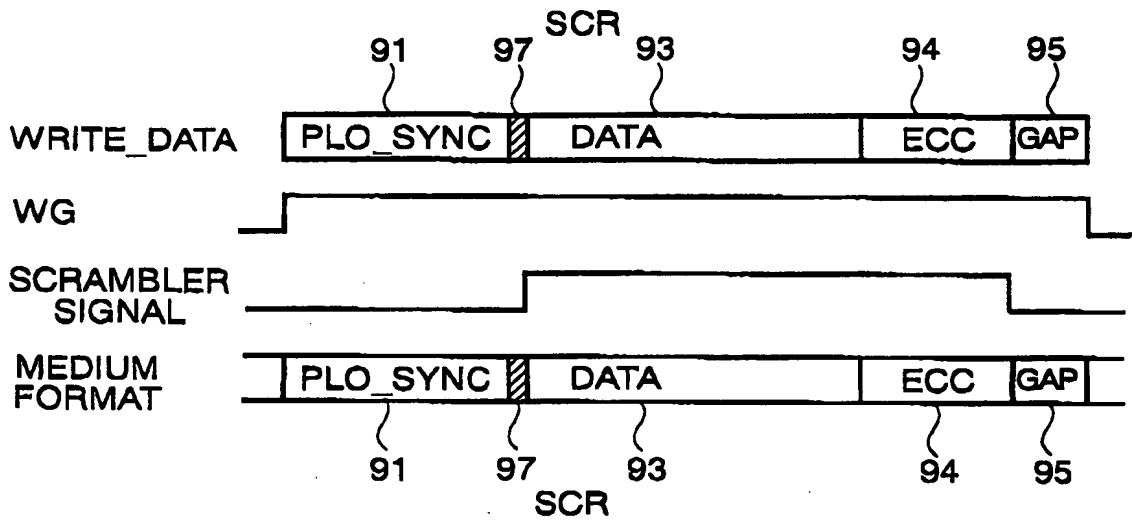


FIG.16

FIG.17

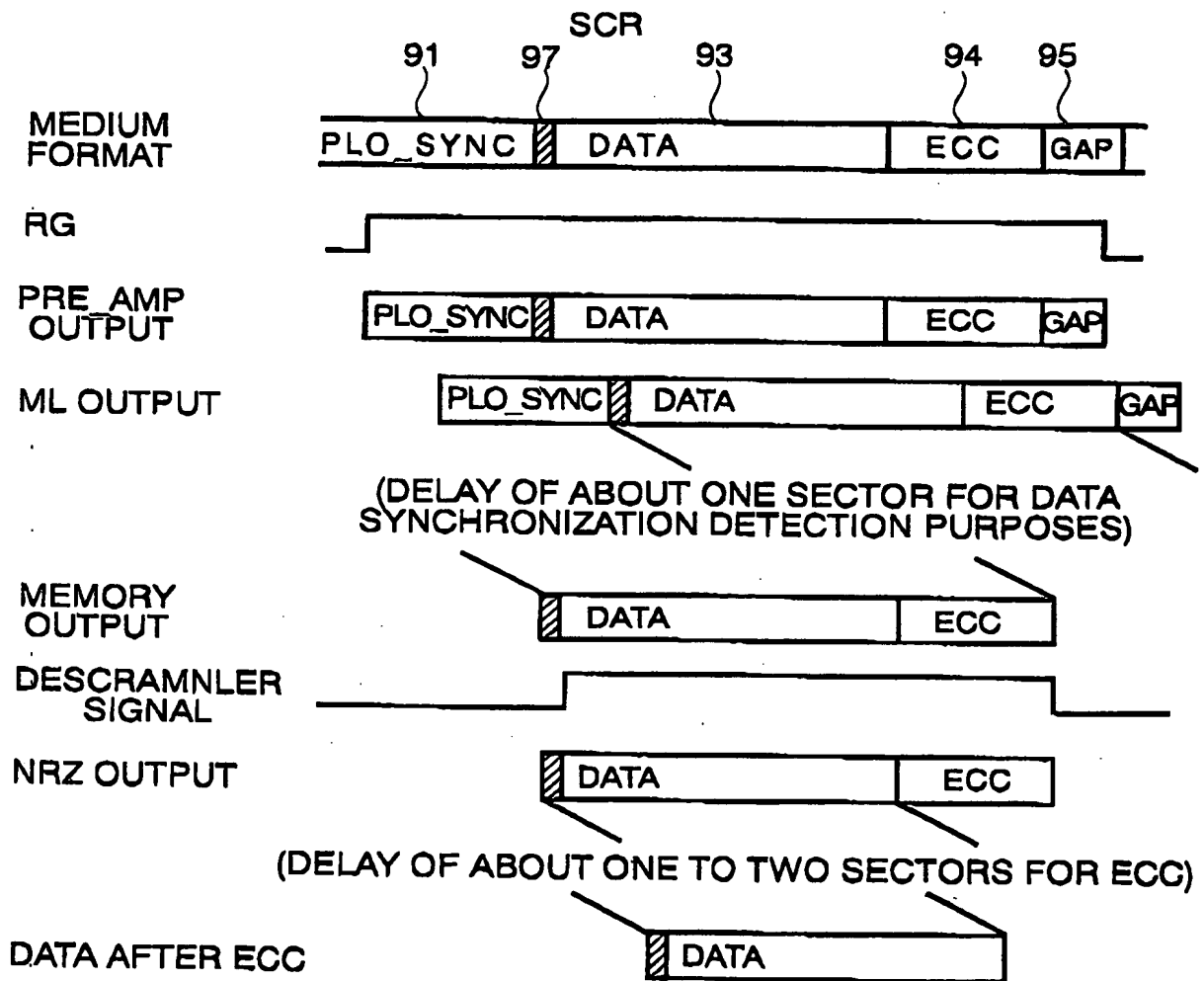


FIG.17

FIG.18

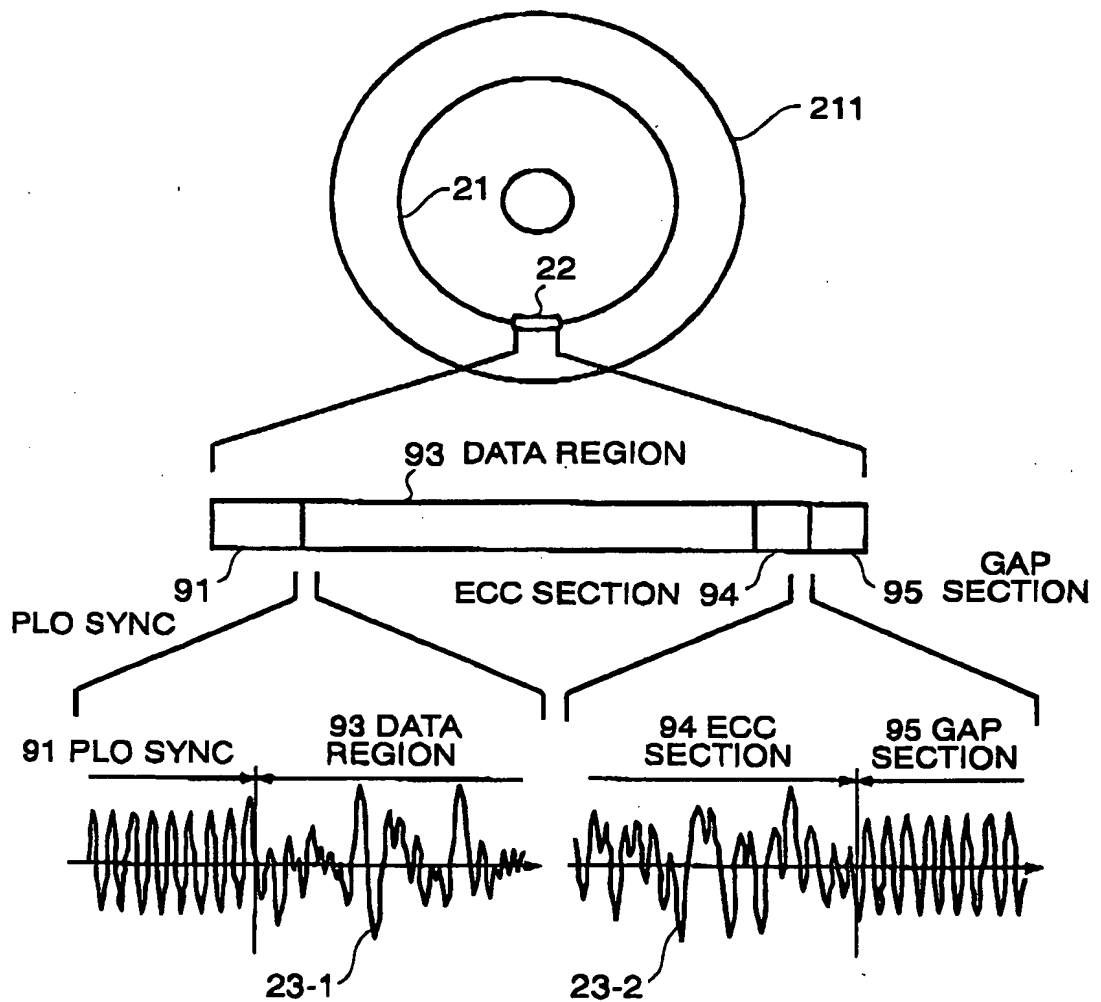


FIG. 19

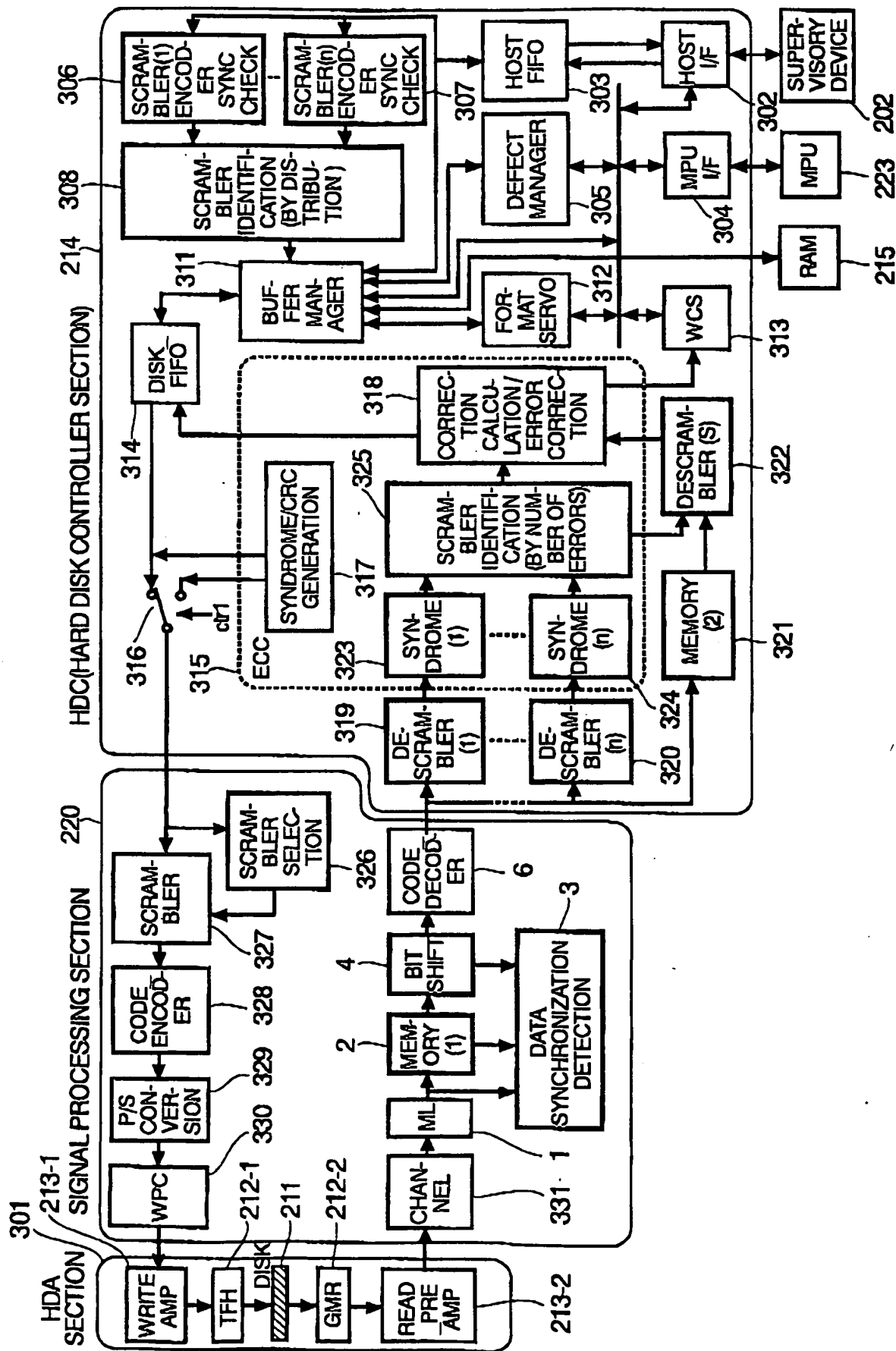


FIG. 20

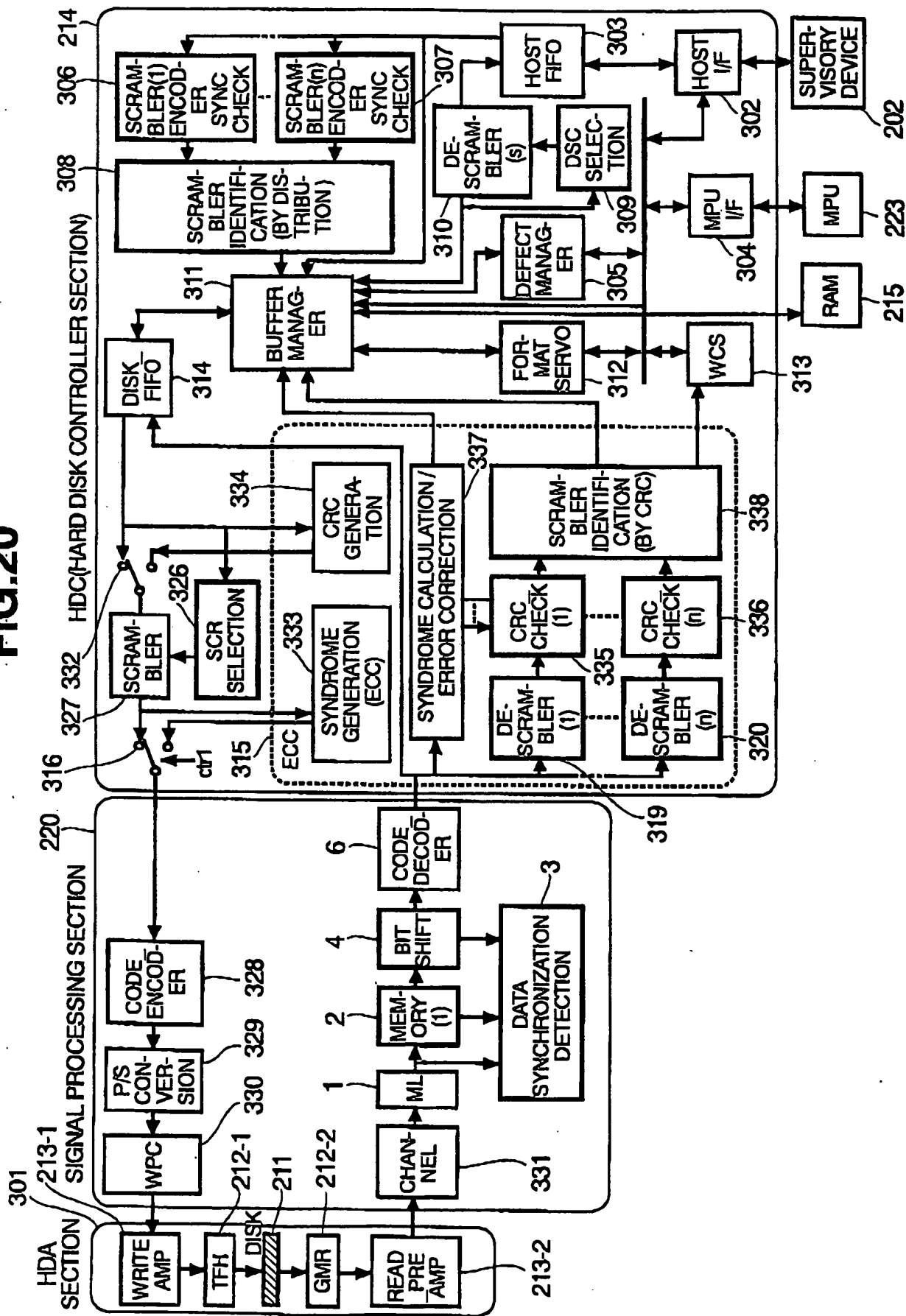


FIG. 21

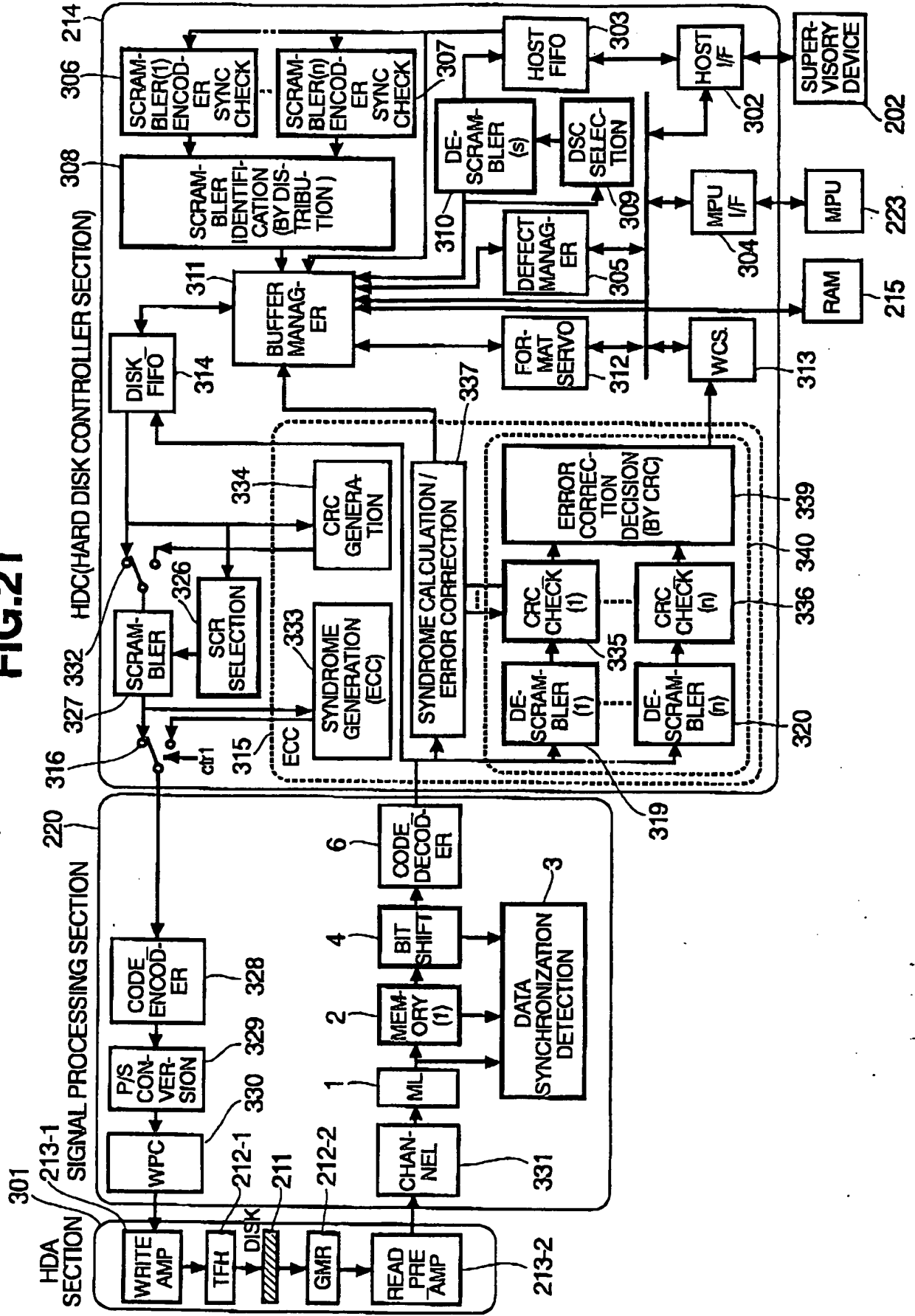


FIG. 22

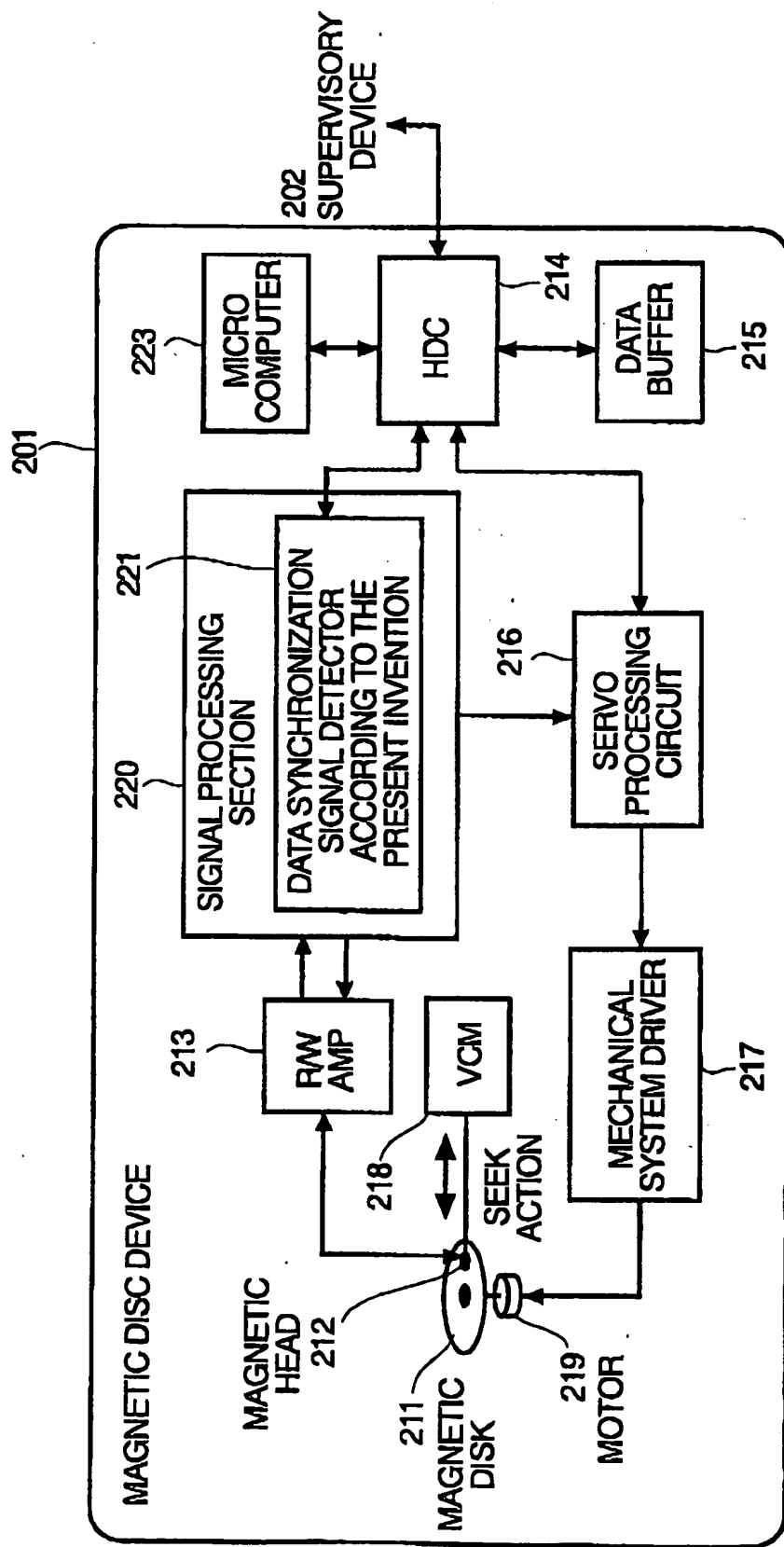


FIG.23

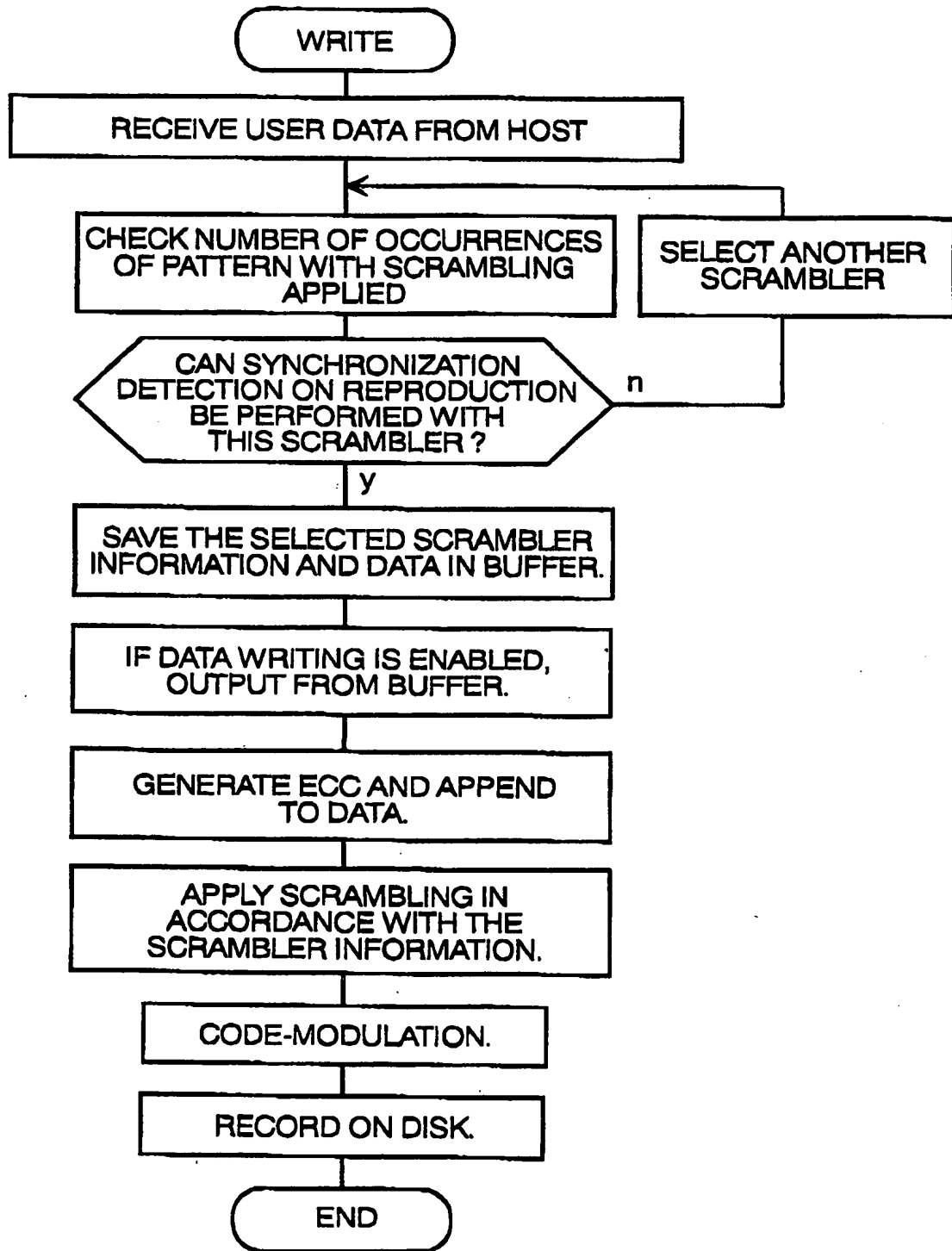


FIG.24

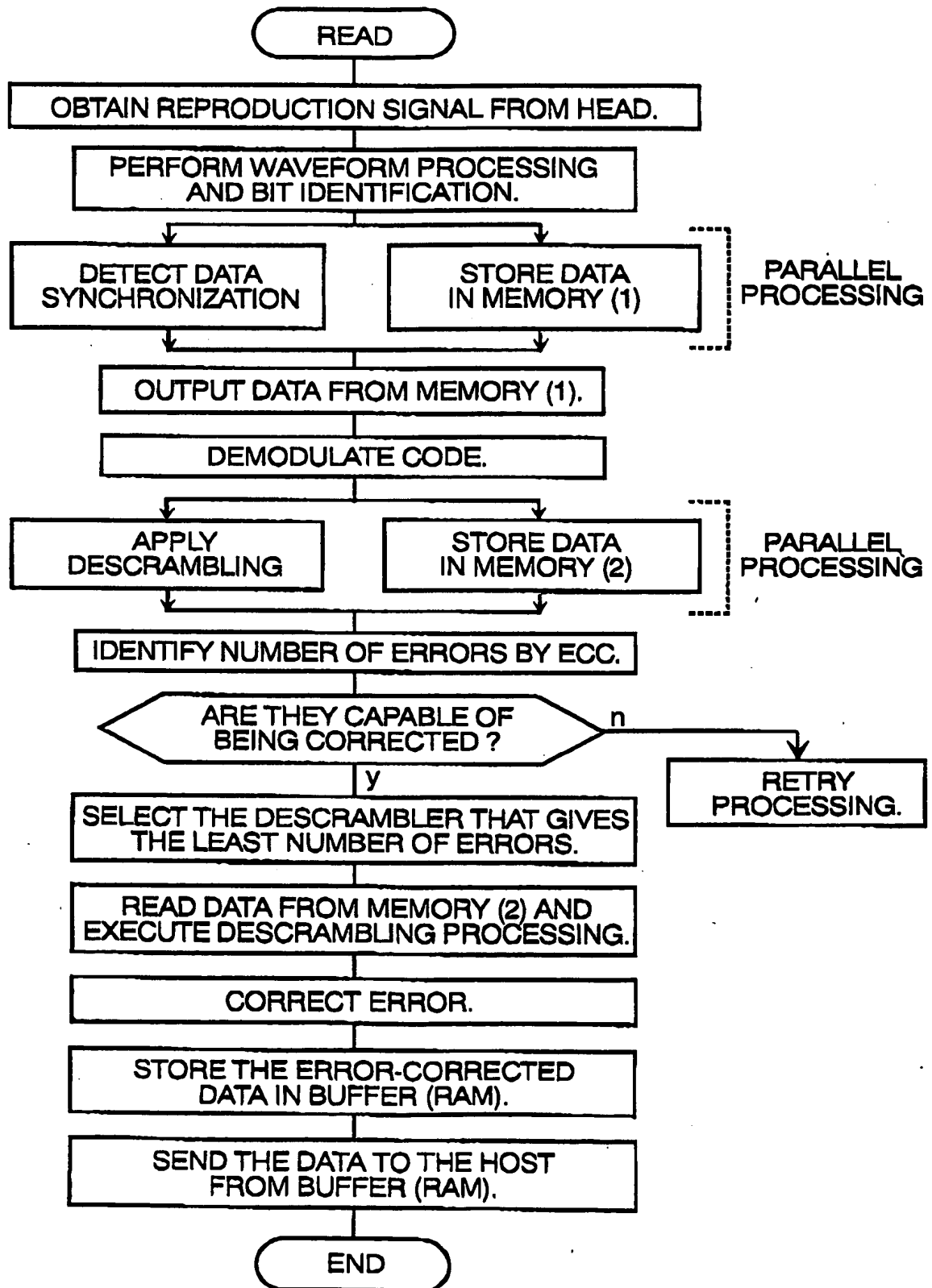


FIG. 24

FIG.25

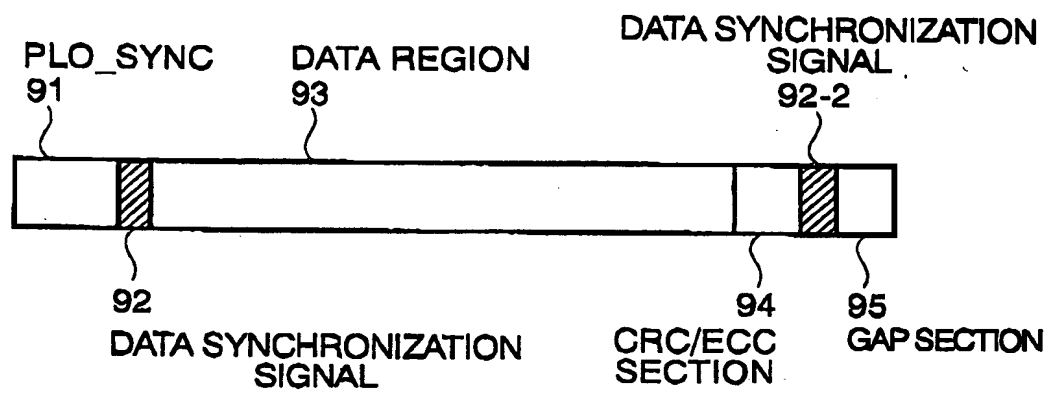


FIG. 25